

# EUROPEAN PATENT OFFICE

## Patent Abstracts of Japan

PUBLICATION NUMBER : 2000194432  
PUBLICATION DATE : 14-07-00

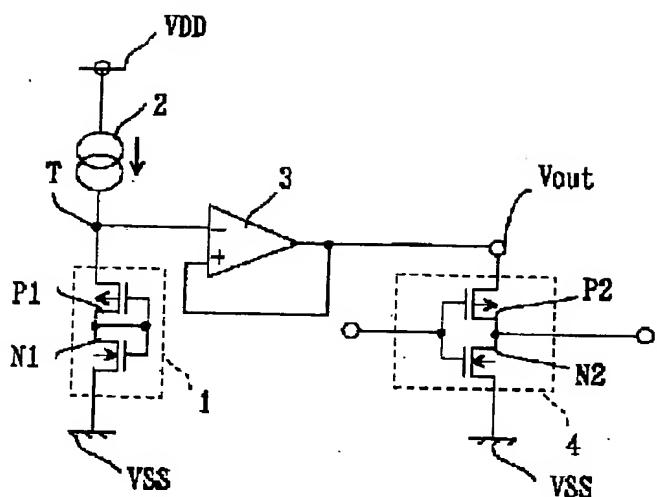
APPLICATION DATE : 25-12-98  
APPLICATION NUMBER : 10369095

APPLICANT : NIPPON PRECISION CIRCUITS INC.

INVENTOR : UENO NAOKI

INT.CL. : G05F 3/24 H03K 19/00

**TITLE : POWER SOURCE CIRCUIT FOR CMOS LOGIC**



**ABSTRACT :** PROBLEM TO BE SOLVED: To control a through-current in CMOS logic and to facilitate to control a consumption current

**SOLUTION:** This circuit is provided with a serial circuit 1 obtained by serially connecting a P channel MOS transistor 1P undergoing diode connection and an N channel MOS transistor 1N undergoing diode connection to each other and a current source 2 serially connected to the serial circuit 1 between power supply terminals VDD and VSS and takes out CMOS logic, e.g. power supply for a CMOS inverter 4 from a connection point between the serial circuit 1 and the current source 2. Much of such logic are usually attached.

COPYRIGHT: (C)2000, JPO